

Art Unit: 2676

CLMPTO-06/08/01

KH-08/27/04

1. A reproduced signal processing apparatus, being an apparatus for processing data reproduced from a recording medium in which data is recorded in sync block units together with the identification information of sync block, at a speed of $\pm \alpha$ times (α being an integer) of usual reproducing speed, comprising:

sync block detecting means for detecting the identification information of the reproduced data,

data information generating means for generating data information composed of track information, field information and frame information from the identification information,

first memory means for storing plural frames of the reproduced data,

memory writing means for writing reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out parallel the data of n frames (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means, and

transmitting means for transmitting n pieces of transmission data by restructuring or without restructuring n pieces of frame data being read out by said memory reading means on the basis of the data information.

2. The reproduced signal processing apparatus of claim 1, wherein said transmitting means either transmits n pieces of frame data being read out by said memory reading means without restructuring, or transmits n pieces of transmission data restructured by selecting m pieces out of n pieces (m being an integer satisfying the relation of $m < n$), according to the data information.

3. The reproduced signal processing apparatus of claim 2, wherein said transmitting means, transmitting one of n pieces of transmission data as main data and others as sub data, can transmit many frame data of data reproduced at α times as main data, and also restructures so as to transmit all frame data reproduced at α times by transmitting all of main data and sub data.

4. (As Amended) The reproduced signal processing apparatus of claim 2, wherein said transmitting means transmits the information showing whether n pieces of frame data to be transmitted are valid or invalid, by adding to the transmission data.

5. The reproduced signal processing apparatus of claim 1, wherein said transmitting means further comprises second memory means for accumulating n pieces of frame data being read out by said memory reading means for the portion of three frames, and

m pieces (m being 1 or 2) of frame data are read out on the basis of the data information, from the data delayed by one frame and two frames, by controlling said second memory, and are restructured into two pieces of transmission data.

6. The reproduced signal processing apparatus of claim 5, wherein said transmitting means restructures the data delayed by one frame and two frames so as to obtain reproduction output of α times at the reception side.

7. (As Amended) The reproduced signal processing apparatus of claim 5, wherein said transmitting means transmits the information showing whether two pieces of frame data to be transmitted are valid or invalid, by adding to the data.

8. A reproduced signal processing apparatus, being an apparatus for processing data reproduced from a recording medium in which data is recorded in sync block units together with the identification information of sync block, at a speed of $\pm\alpha$ times (α being an integer) of usual reproducing speed,

Art Unit: 2676

comprising:

sync block detecting means for detecting the identification information of the reproduced data,

data information generating means for generating data information composed of track information, field information and frame information from the identification information,

first memory means for storing plural frames of the reproduced data,

memory writing means for writing the reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out parallel the data of n frames (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means,

delay means for issuing n pieces of frame data being read out by said memory reading means by delaying by one field and two fields each, and also issuing data of n -th frame by delaying by three fields, and

reproduction output control means for selecting and issuing outputs of said delay means and memory reading means in field units on the basis of the data information.

9. A reproduced signal processing apparatus, being an apparatus for processing data reproduced from a recording medium in which data is recorded in sync block units together with the identification information of sync block, at a speed of $\pm\alpha$ times (α being an integer) of usual reproducing speed, comprising:

sync block detecting means for detecting the identification information of the reproduced data,

data information generating means for generating data information composed of track information, field information and frame information from the identification information,

first memory means for storing plural frames of the

Art Unit: 2676

reproduced data,

memory writing means for writing the reproduced data in said first memory means on the basis of the identification information,

memory reading means for reading out parallel the data of n frames (n being an integer of 2 or more satisfying the relation of $\alpha \leq n$) accumulated in said first memory means,

second memory means for accumulating n pieces of frame data being read out by said memory reading means for the portion of three frames each, and

reproduction output control means for selecting and issuing field data on the basis of the data information, from the data delayed by one frame and two frames by controlling said second memory means.

10. (Newly Added) The reproduced signal processing apparatus of claim 3, wherein said transmitting means transmits the information showing whether n pieces of frame data to be transmitted are valid or invalid, by adding to the transmission data.

11. (Newly Added) The reproduced signal processing apparatus of claim 6, wherein said transmitting means transmits the information showing whether two pieces of frame data to be transmitted are valid or invalid, by adding to the data.